**Vishwakarma Institute of Information Technology, Pune-48**

**Department of Computer Engineering**

2 Aug 2021

**TY BTech Pattern 2018**

**Language Processor and Compiler Construction (LPCC) Lab**

**List of Assignments**

Note: Students can implement the following assignments using LEX/YACC, C/C++, JAVA, Python)

| **S#** | **Details** | **Start Date** | **End Date** |
| --- | --- | --- | --- |
| 1 | Generate Symbol table, Literal table, Pool table & Intermediate code of a two-pass Assembler for the given source code. | 16 Aug 2021 | 3 Sept 2021 |
| 2 | Design suitable data structures & implement pass-I of a two-pass Macro processor. | 6 Sept 2021 | 17 Sept 2021 |
| 3 | Write a program to implement a lexical analyzer for parts of speech. Using LEX. | 20 Sept 2021 | 24 Sept 2021 |
| 4 | Write a program to evaluate an arithmetic expression, built-in functions, and variables using YACC specification. | 27 Sept 2021 | 1 Oct 2021 |
| 5 | Write a program to generate three address code for the simple expression. | 11 Oct 2021 | 15 Oct 2021 |

**Manisha Mali**

**Mrs. P. R. Rege**

**Mr. V A Meshram**